

FIG. 1

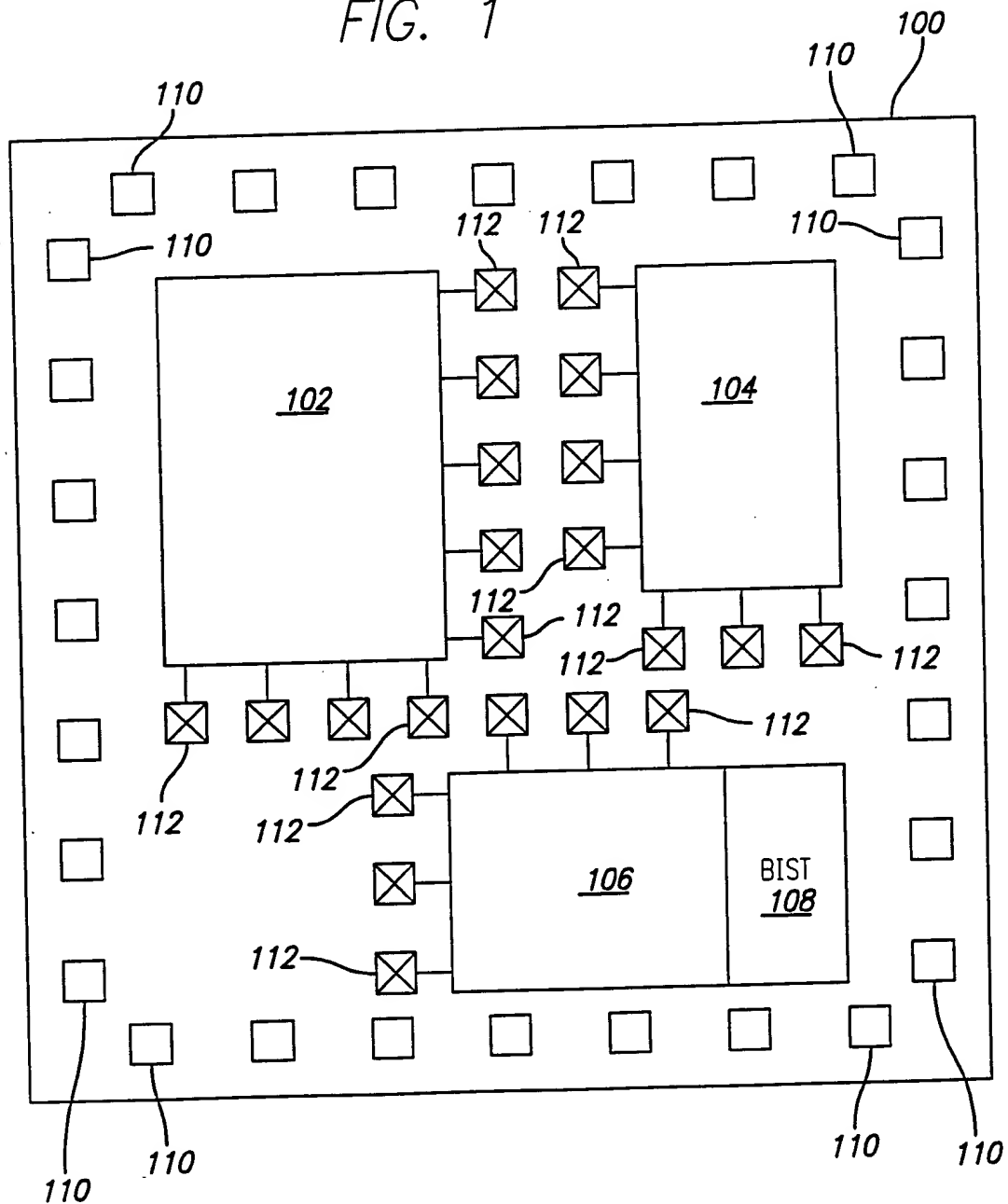


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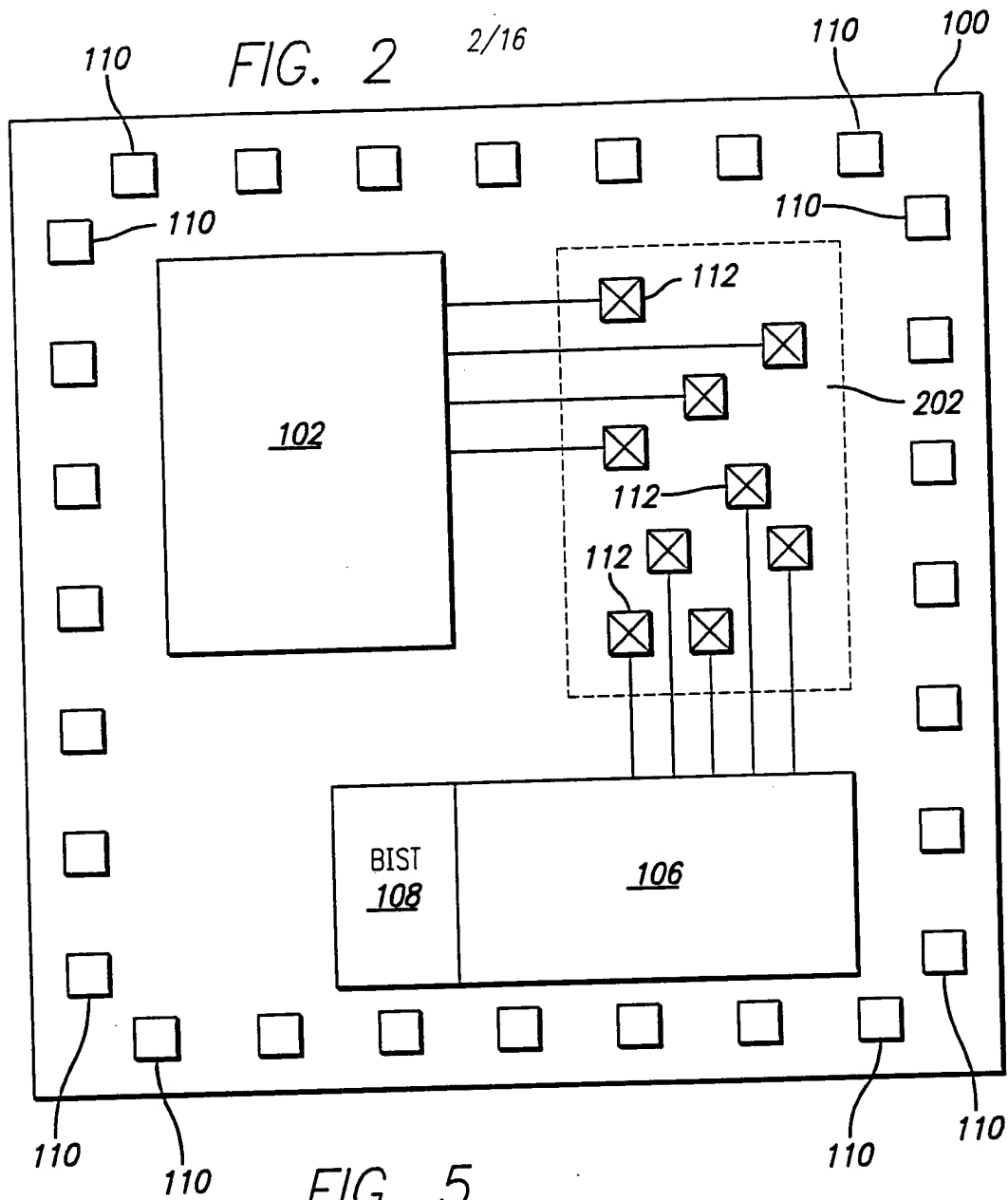
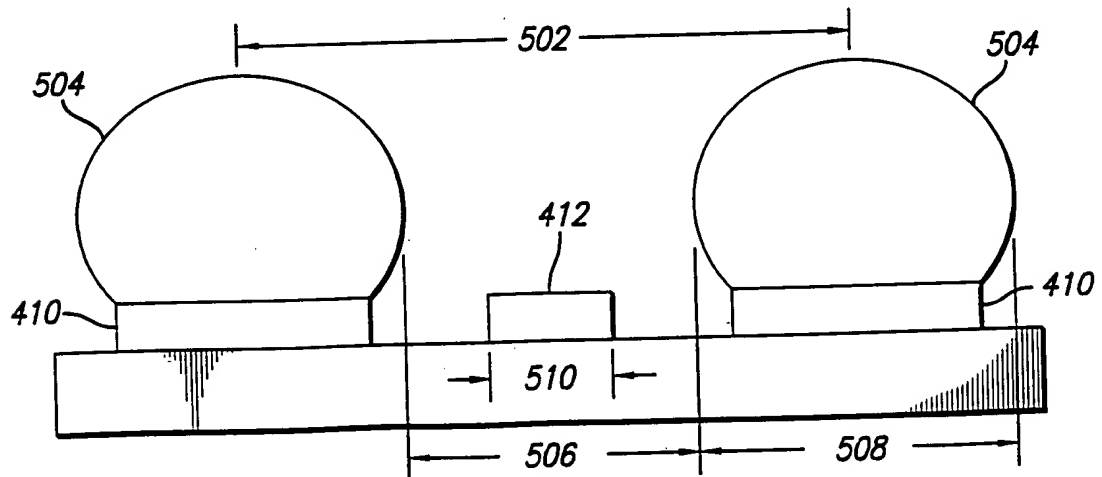


FIG. 5



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FIG. 3A

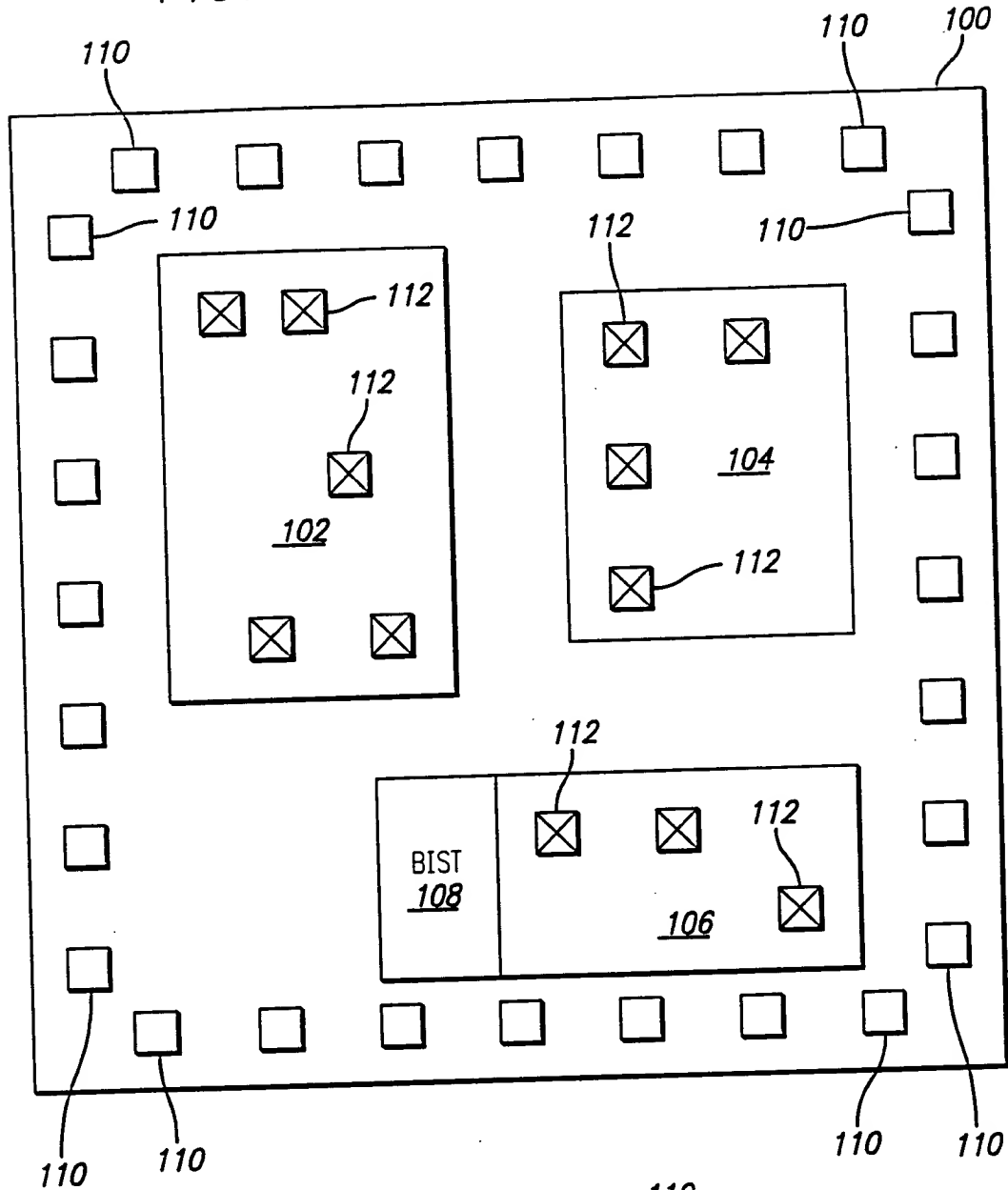


FIG. 3B

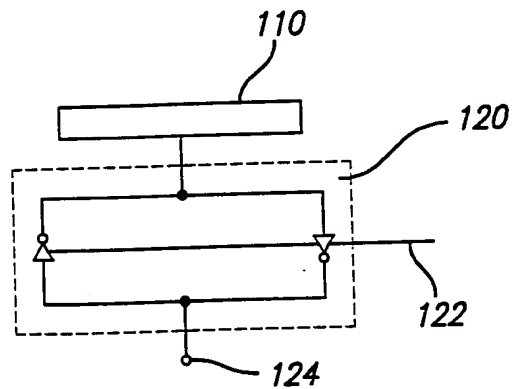


FIG. 3A

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FIG. 4

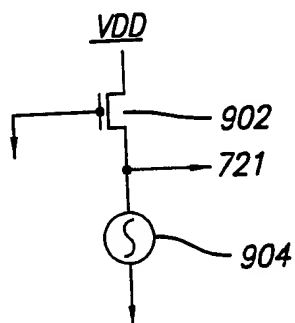
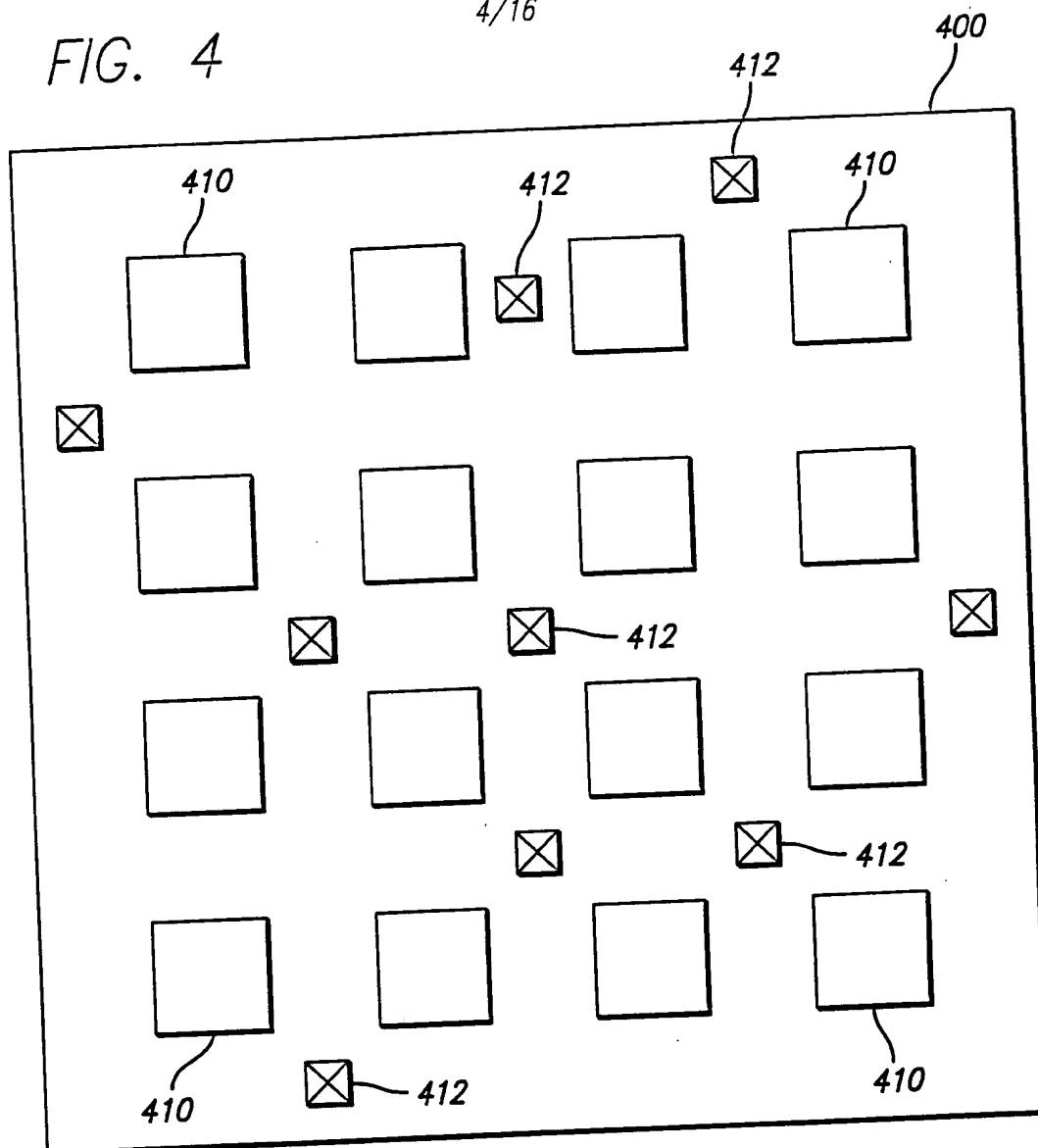


FIG. 9

FIG. 4

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FIG. 6

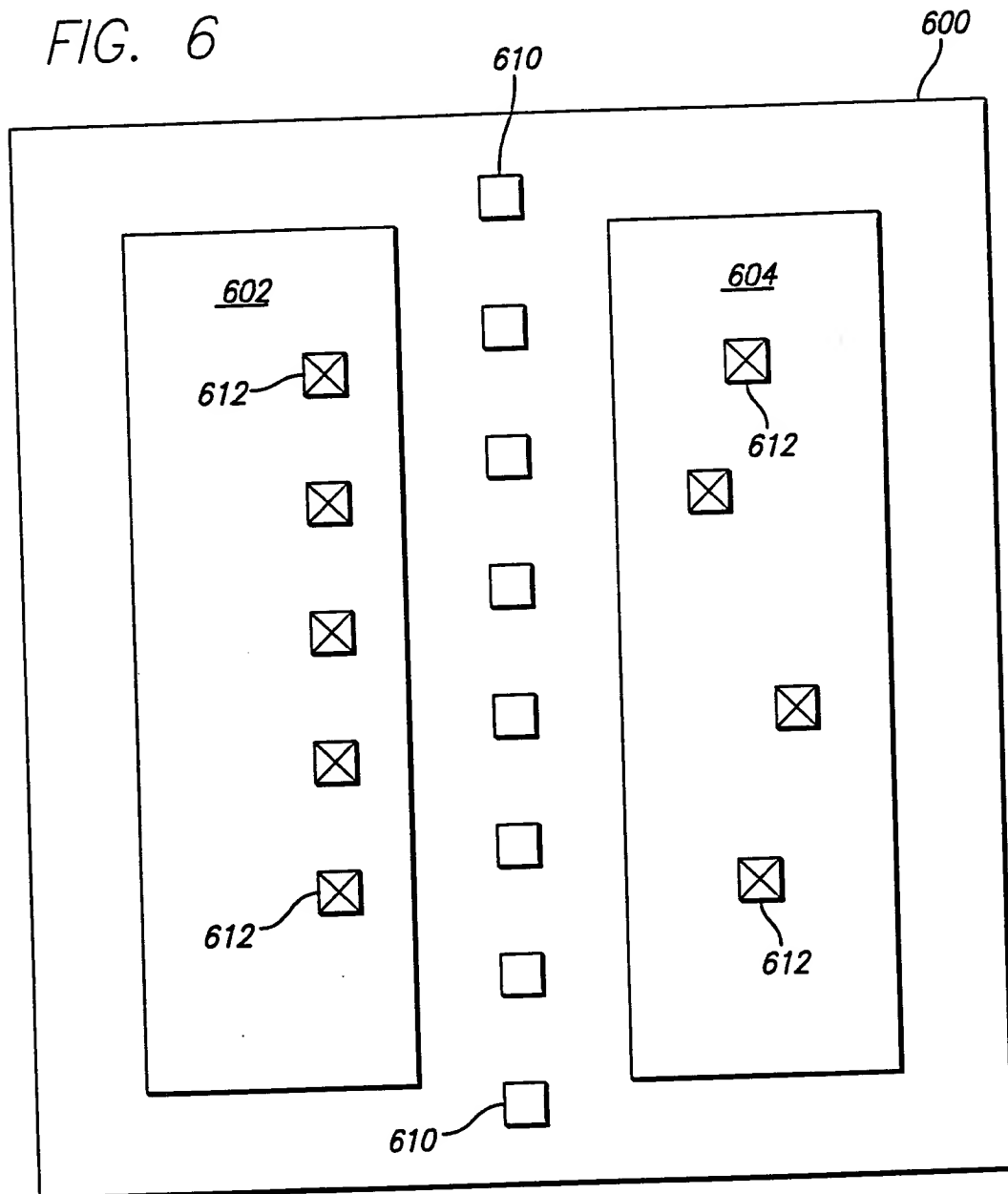


FIG. 6

FIG. 7

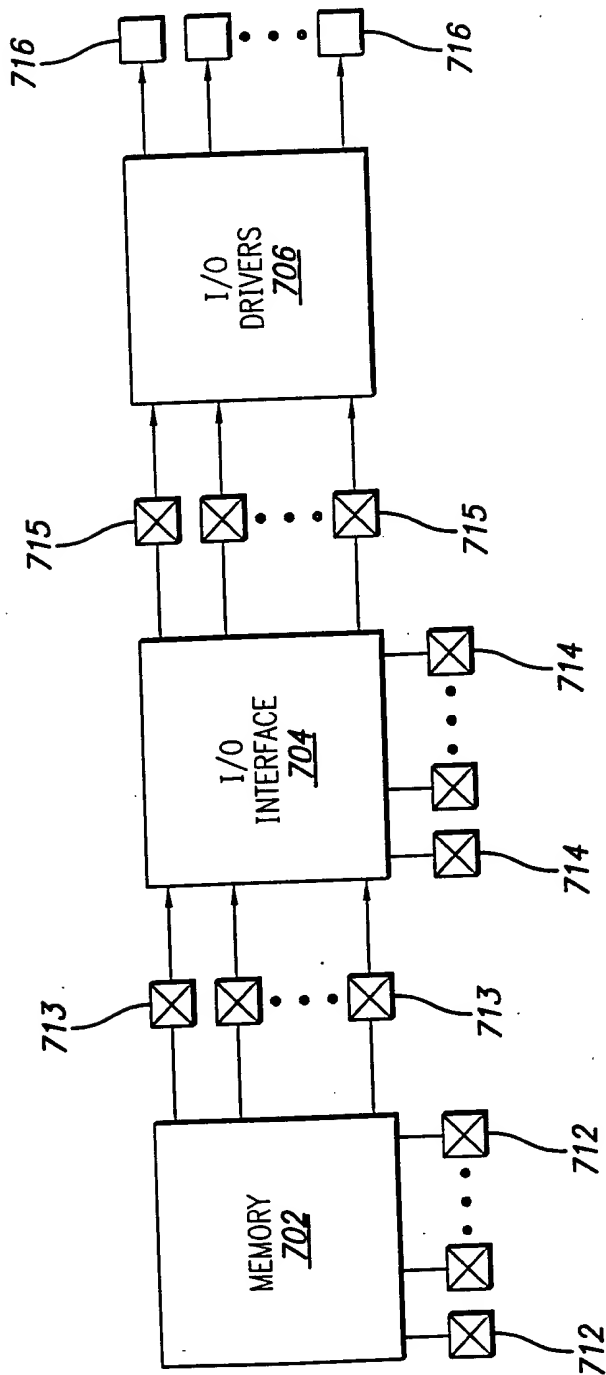


FIG. 7

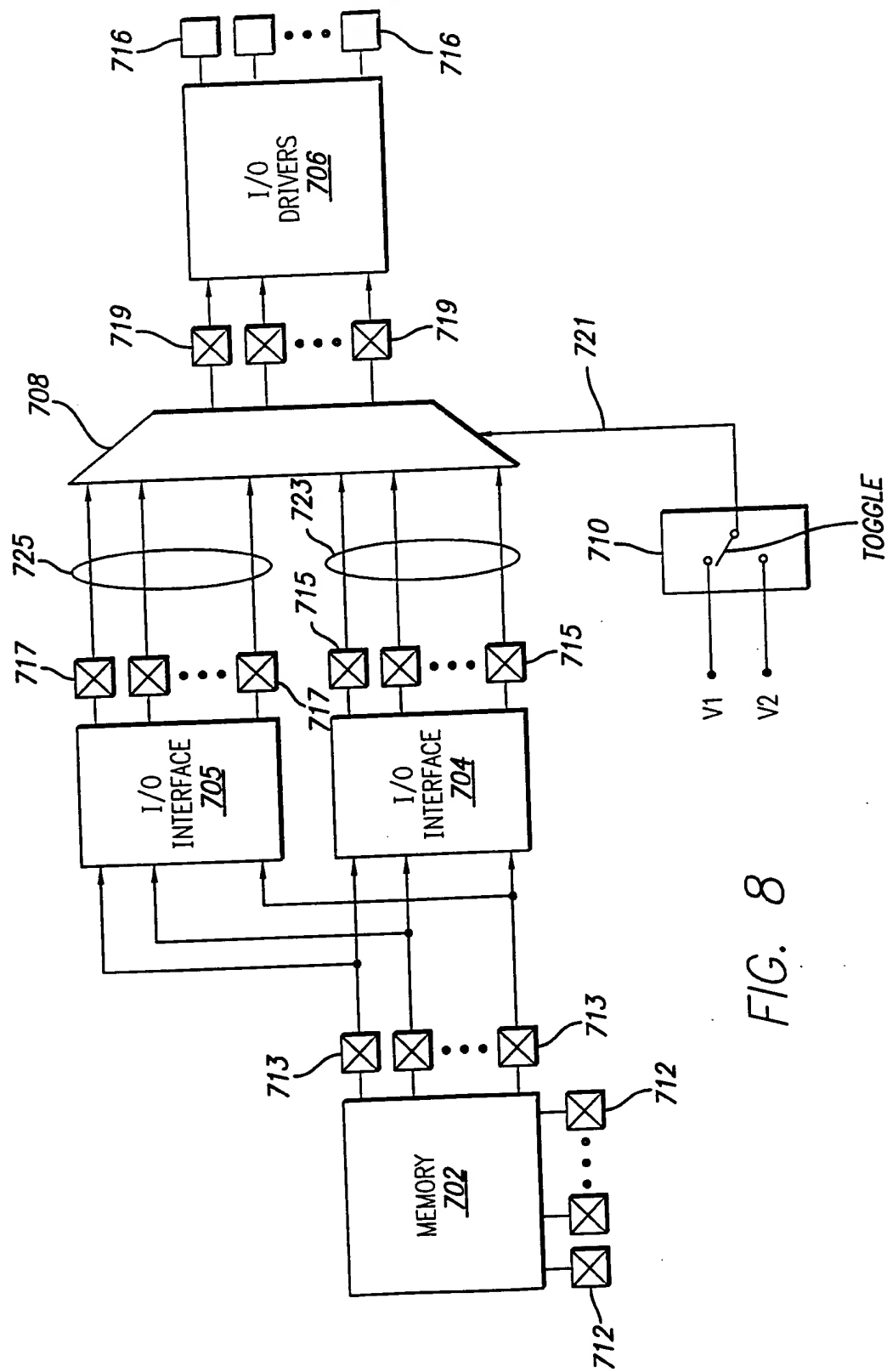


FIG. 8

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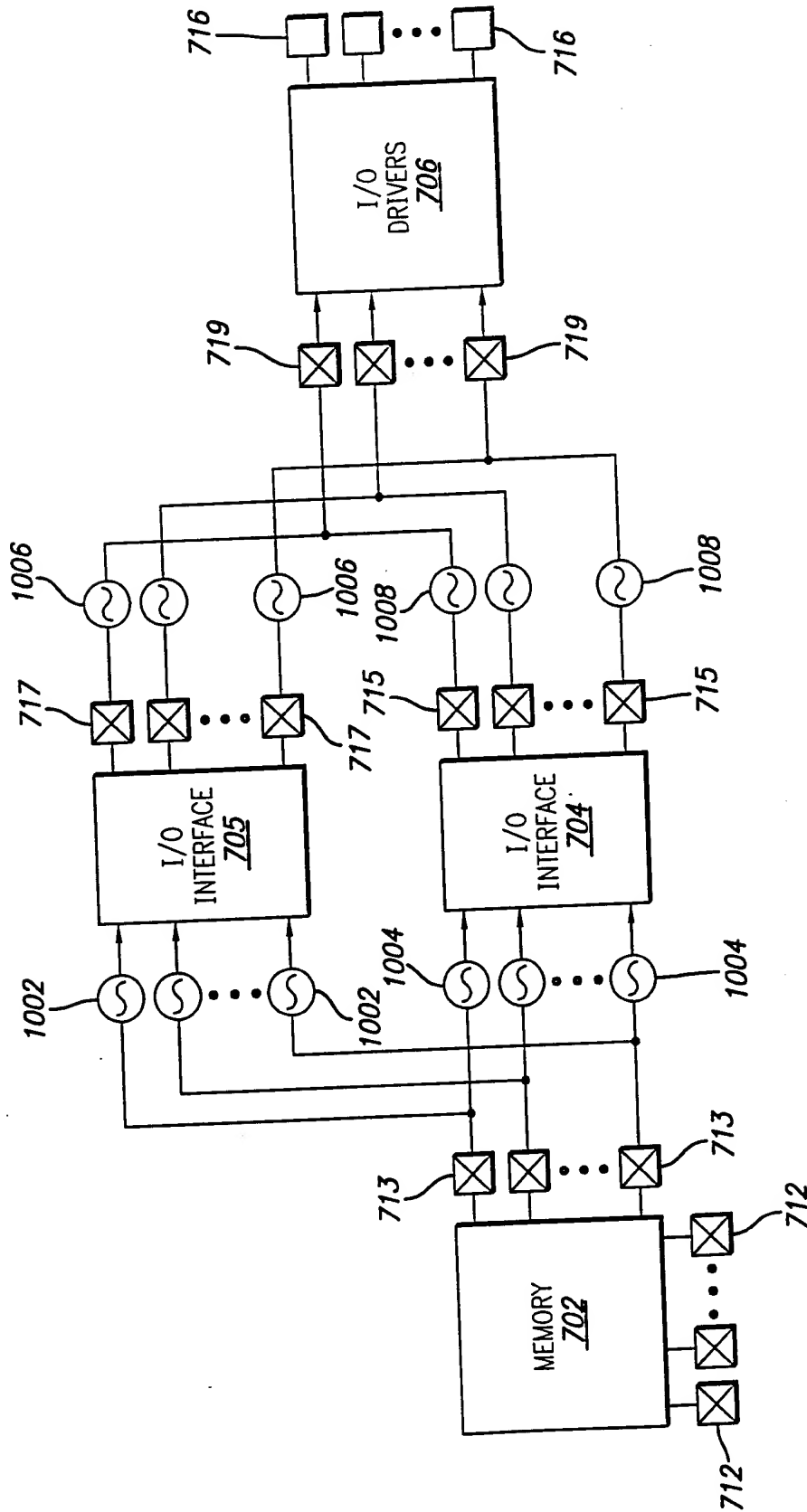


FIG. 10

FIG. 10 is a block diagram of a system architecture.



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FIG. 11

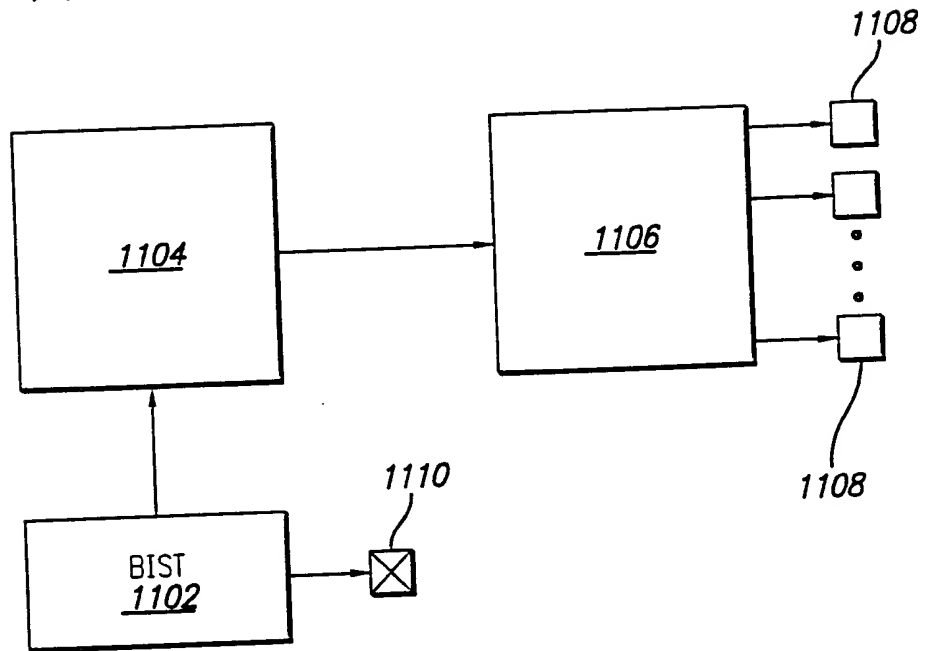
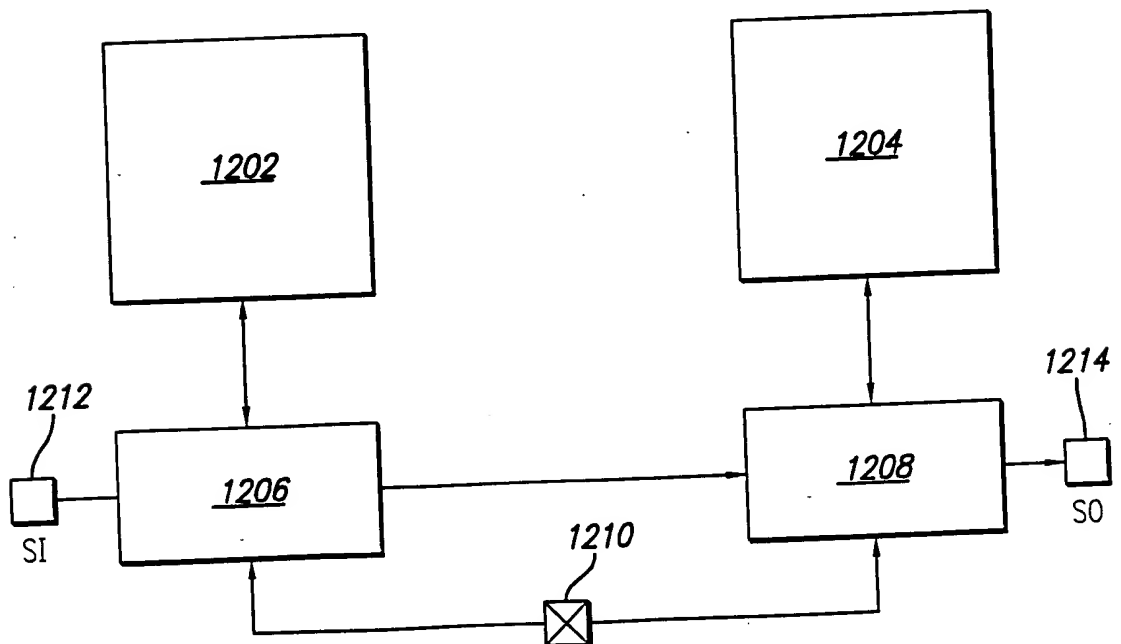


FIG. 12



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FIG. 13

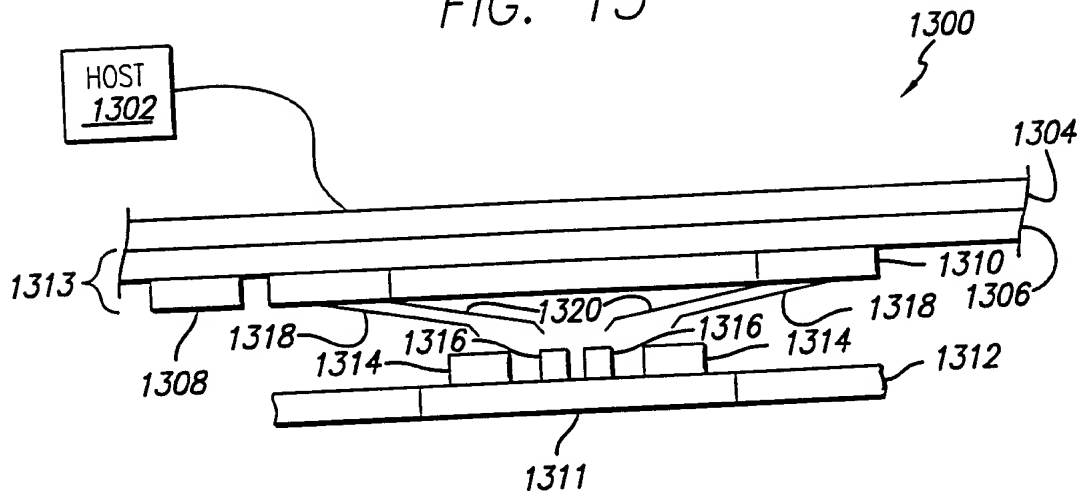
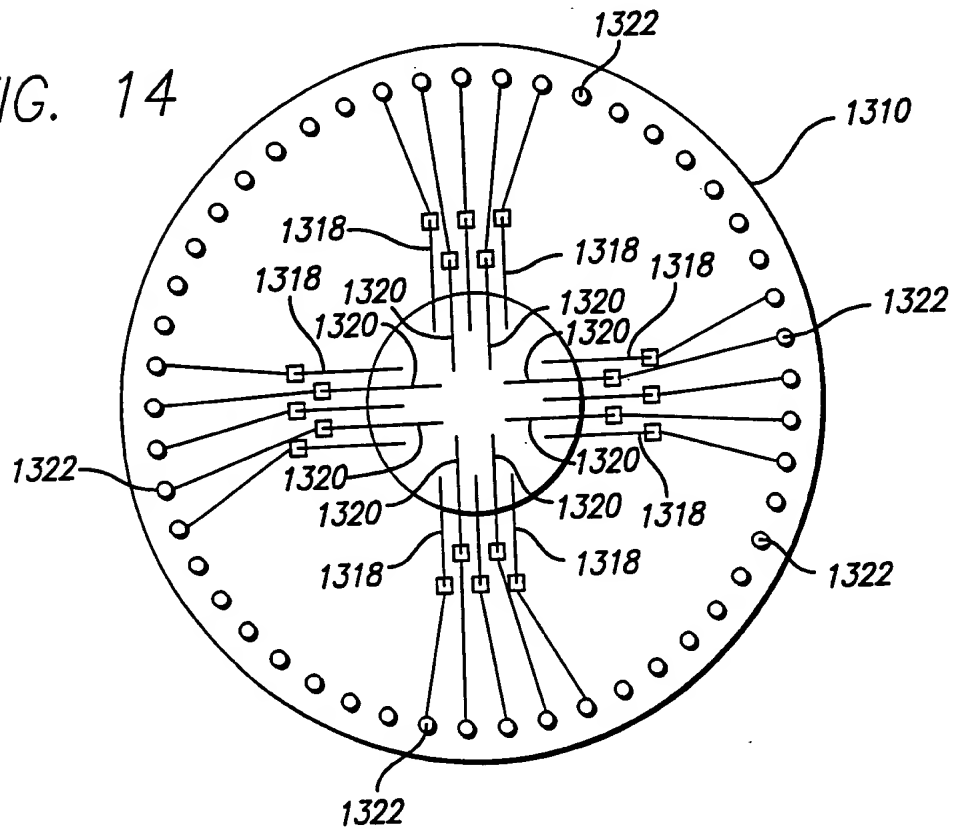


FIG. 14



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FIG. 15

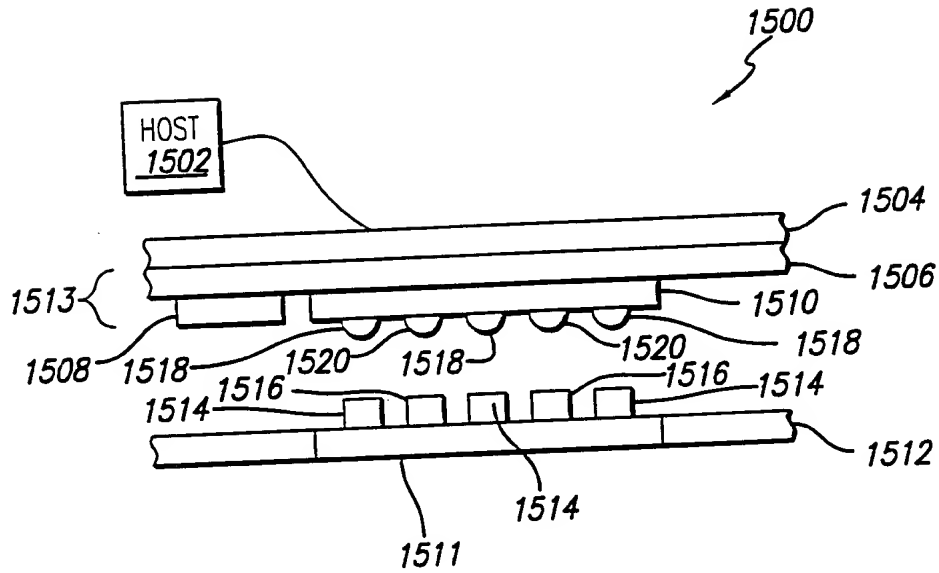


FIG. 16

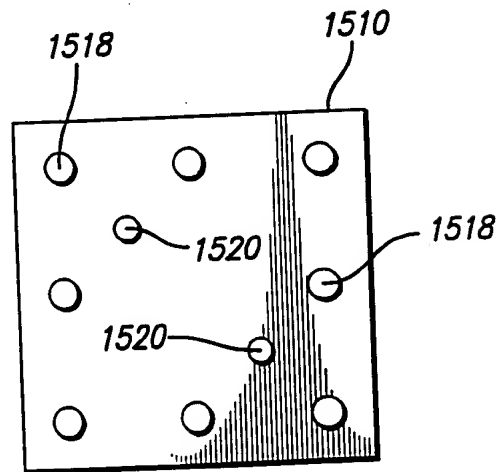
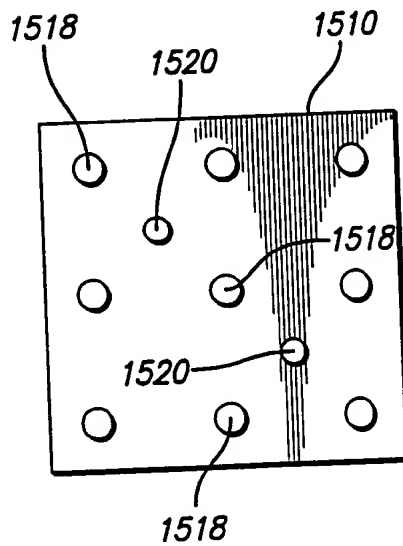


FIG. 17

FIG. 15

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FIG. 18

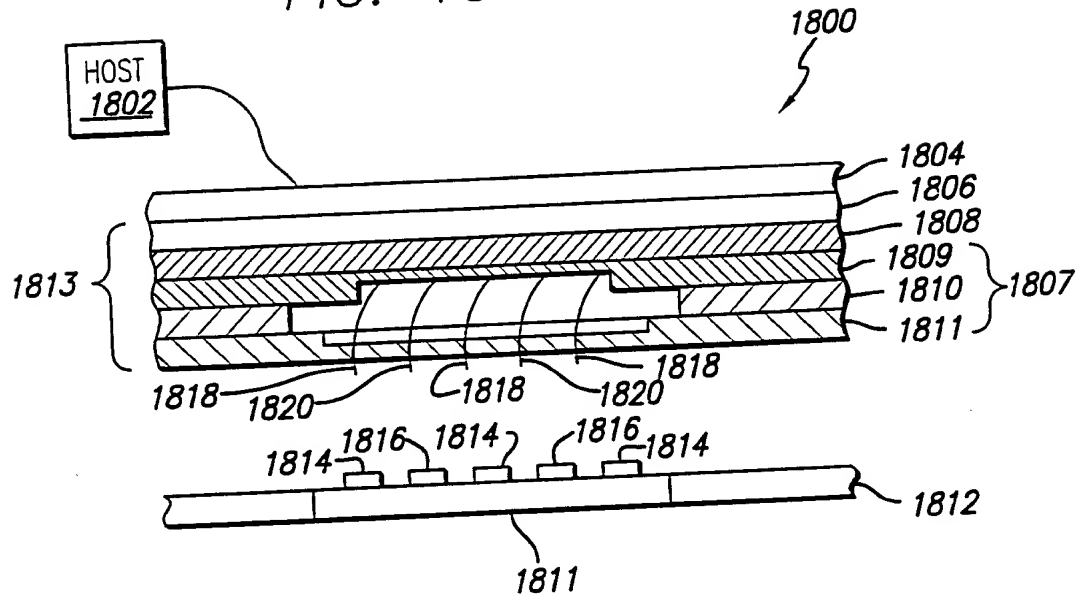


FIG. 19

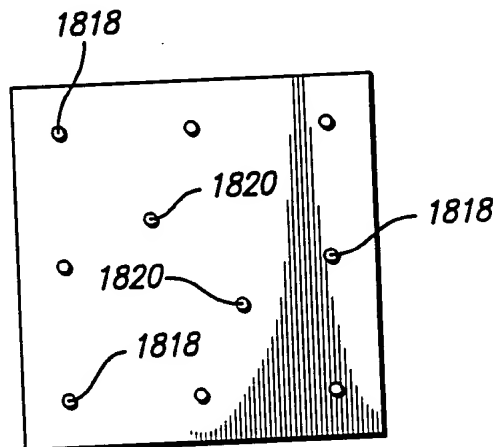
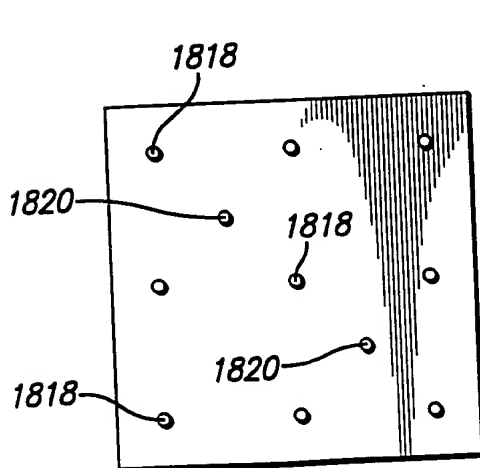


FIG. 20

FIG. 18

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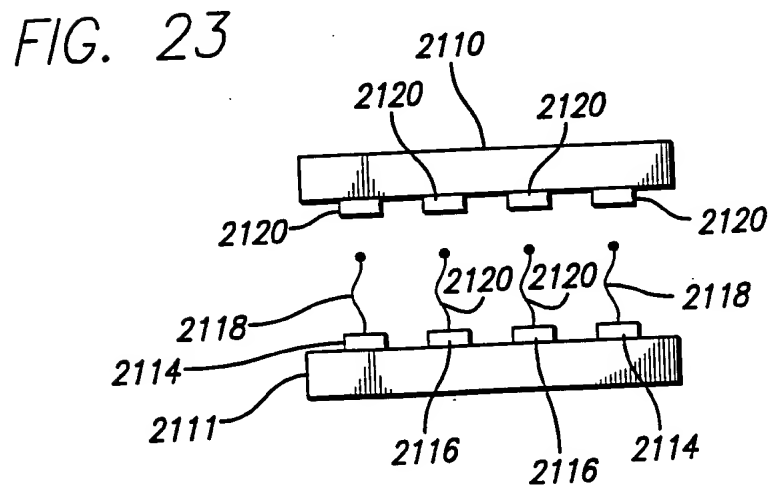
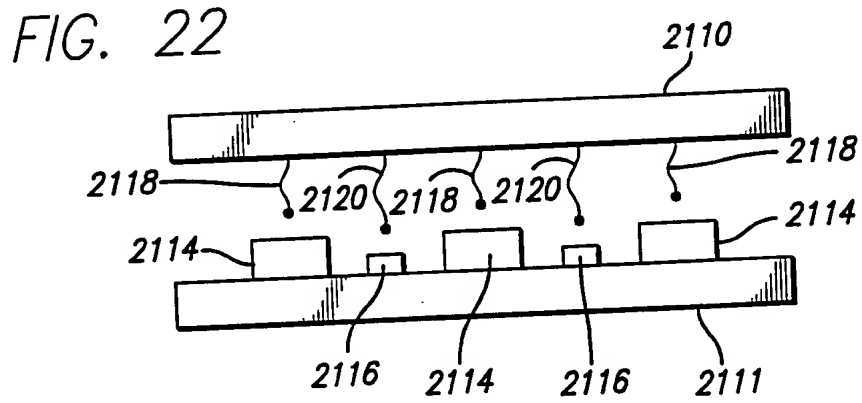
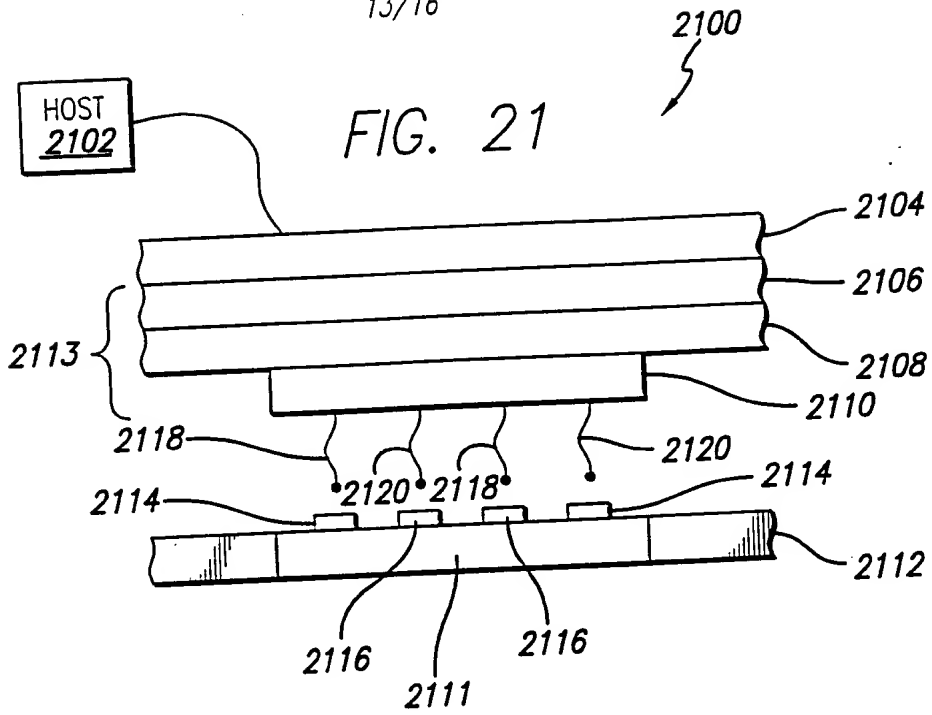


FIG. 21

FIG. 24

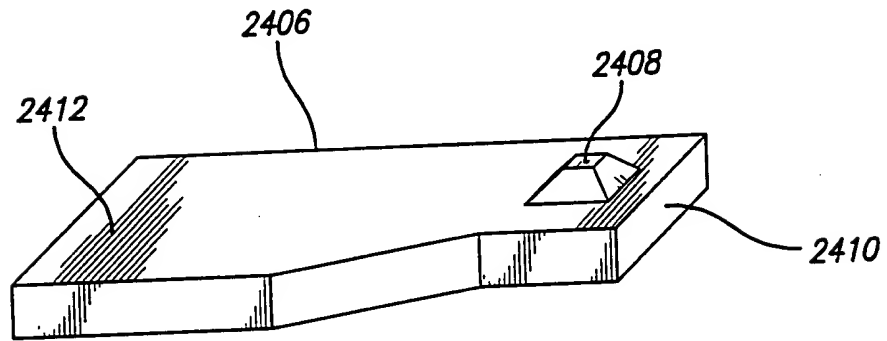
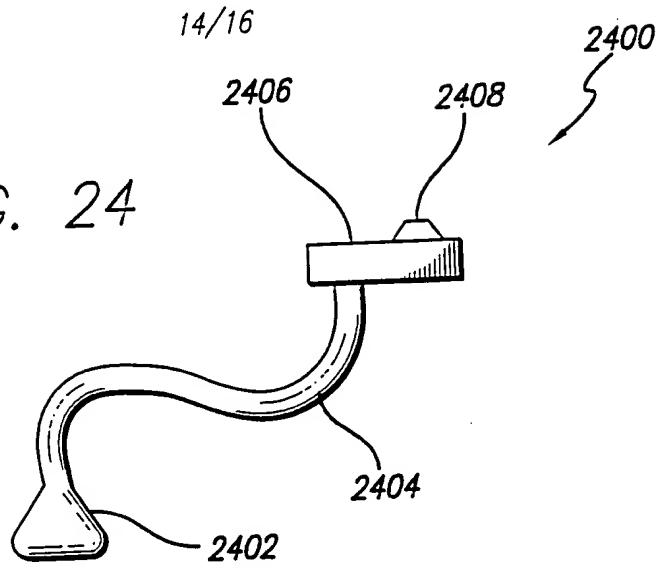


FIG. 25

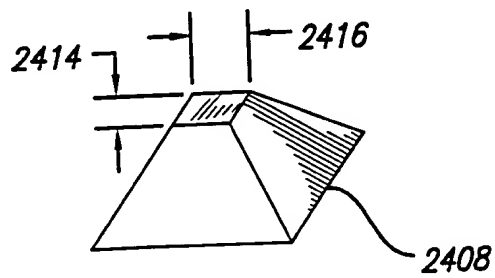


FIG. 26

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FIG. 27

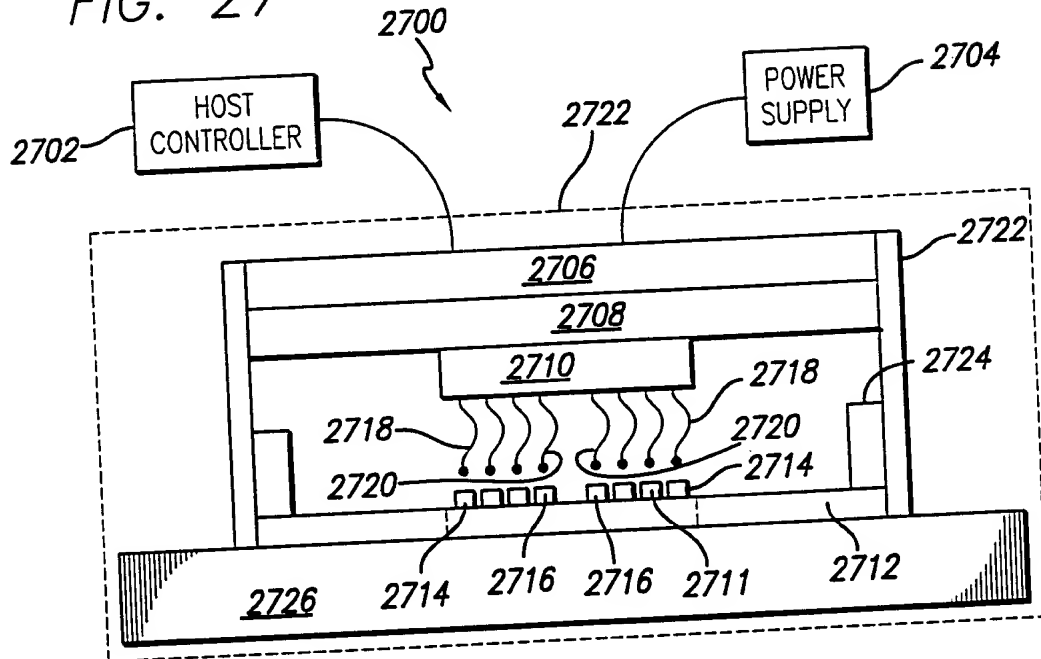
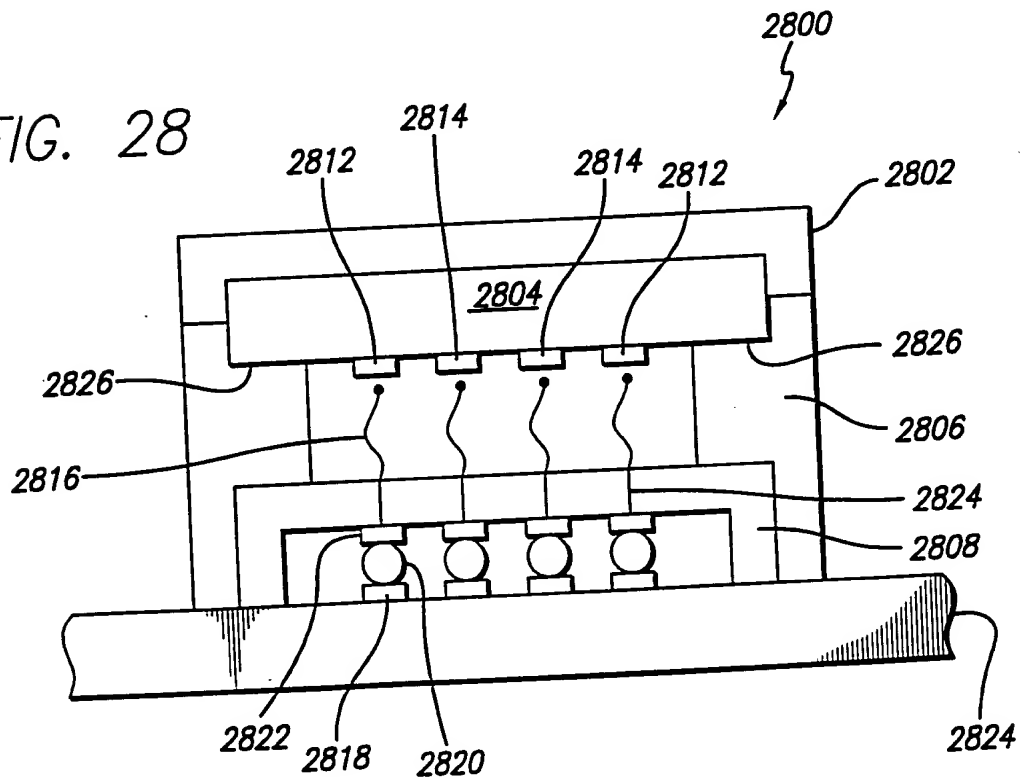


FIG. 28



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FIG. 29A

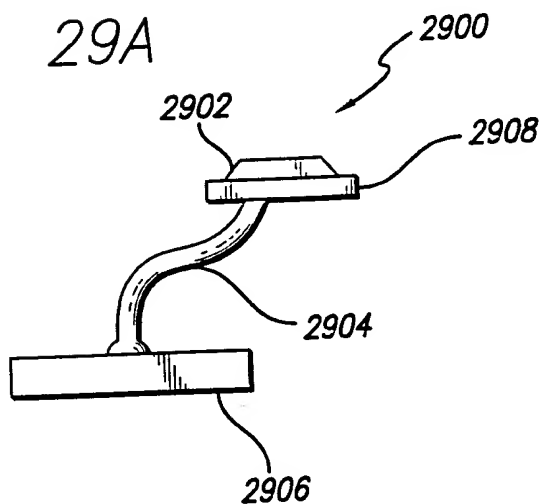


FIG. 29B

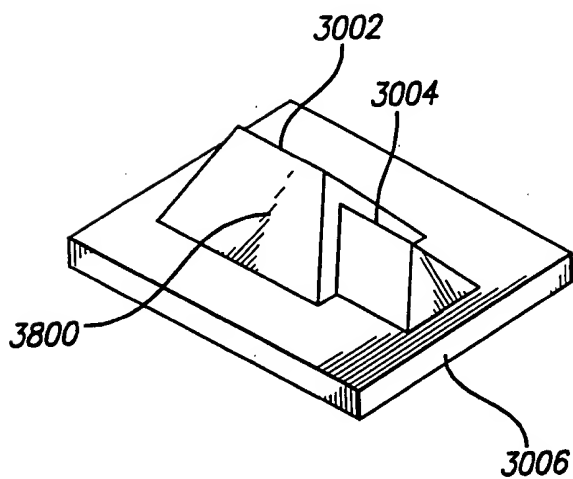
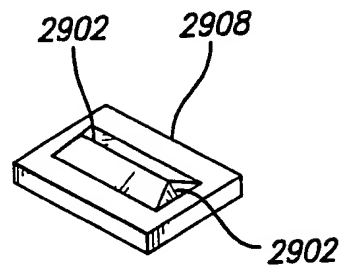


FIG. 30A

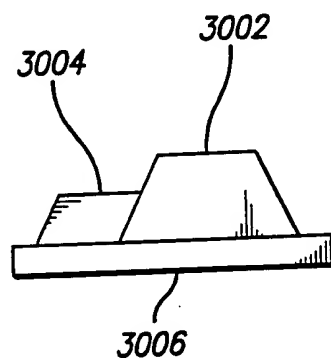
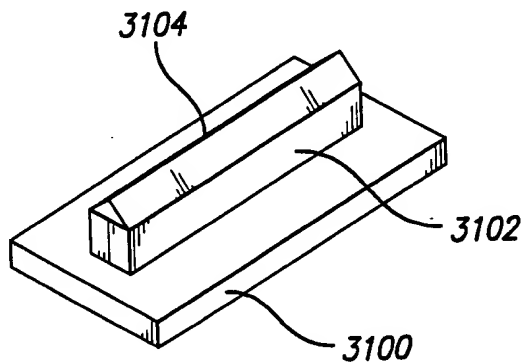


FIG. 30B

FIG. 31



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